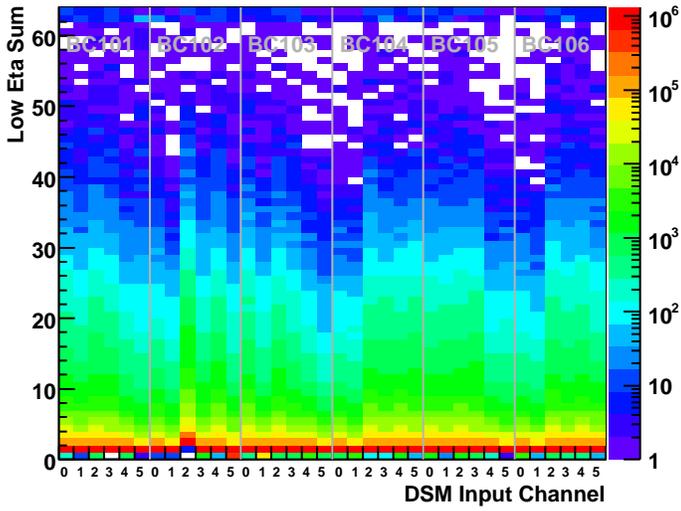


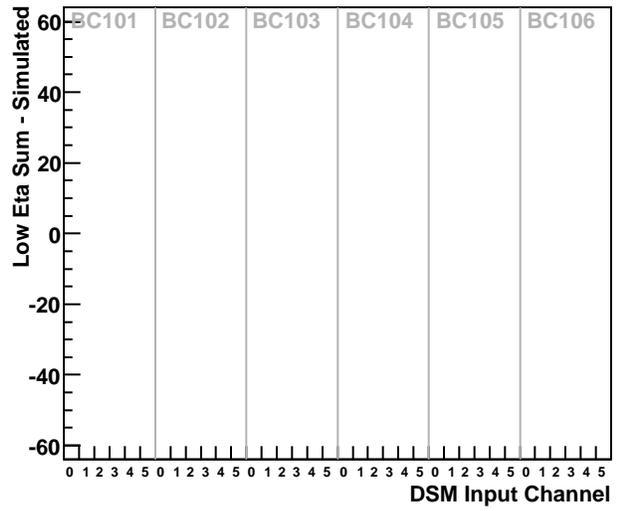
Barrel EMC L1 Input - Low Eta Sum

Entries 5.04e+07



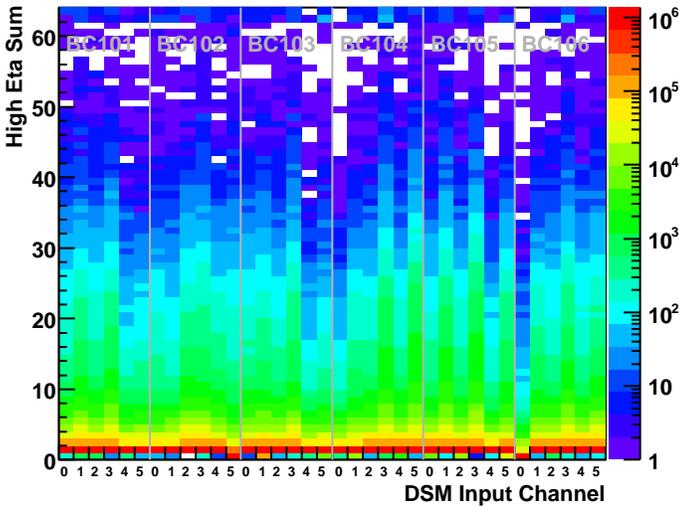
Barrel EMC L1 Input - Low Eta Sum

Entries 0



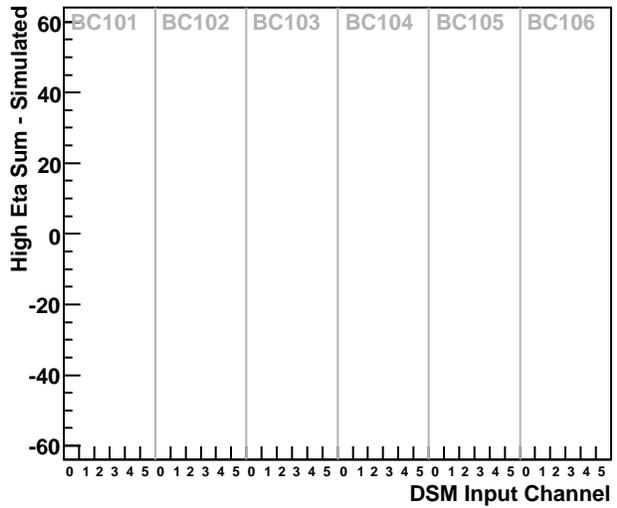
Barrel EMC L1 Input - High Eta Sum

Entries 5.04e+07



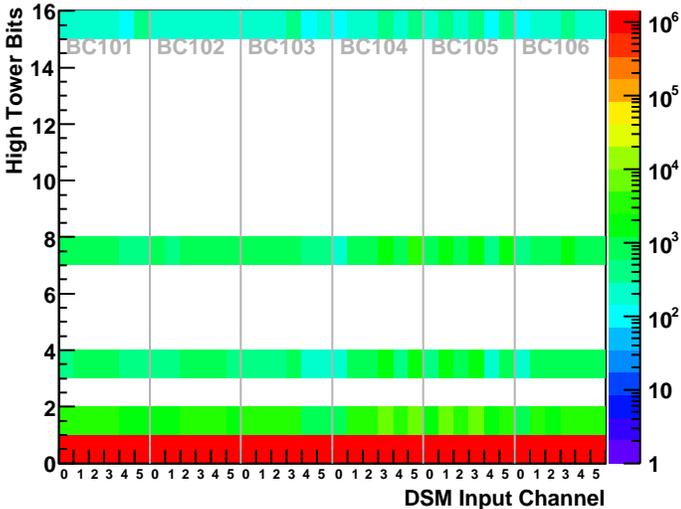
Barrel EMC L1 Input - High Eta Sum

Entries 0



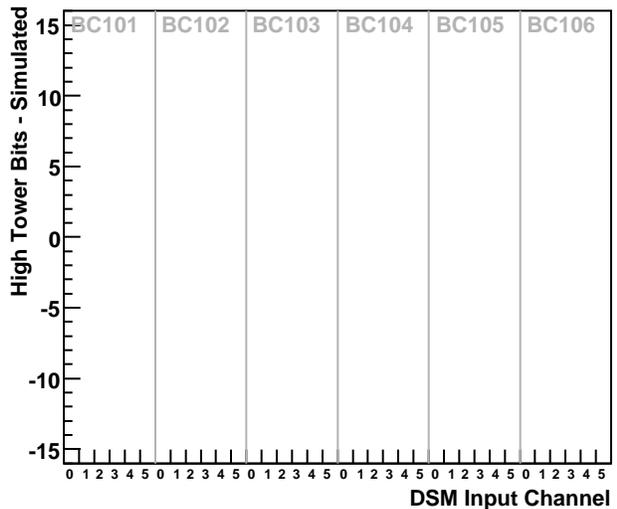
Barrel EMC L1 Input - High Tower Bits

Entries 5.04e+07



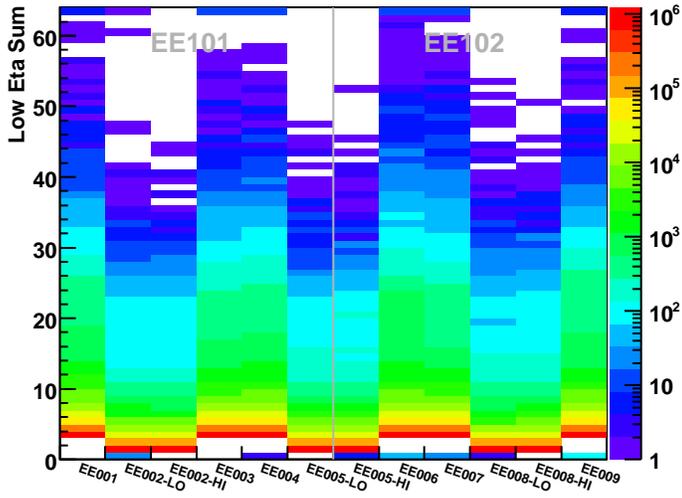
Barrel EMC L1 Input - High Tower Bits

Entries 0



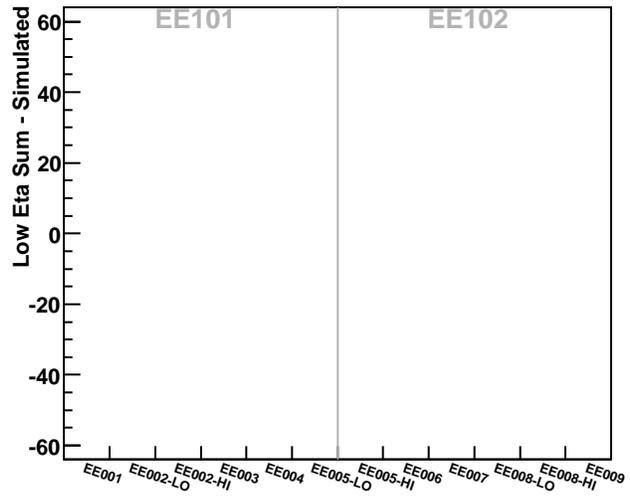
Endcap EMC L1 Input - Low Eta Sum

Entries 1.68e+07



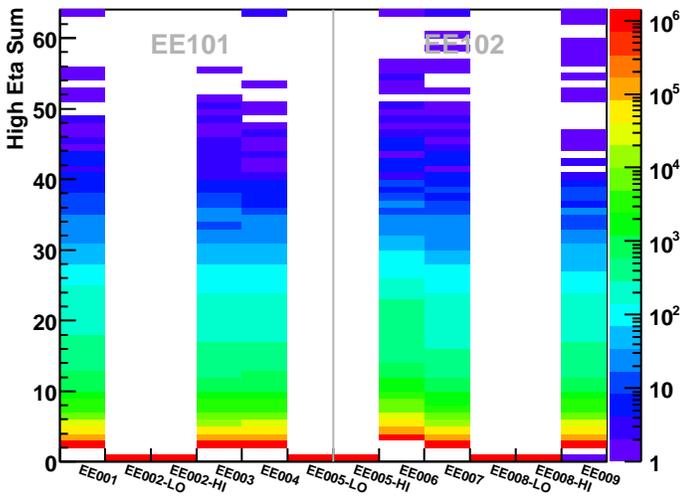
Endcap EMC L1 Input - Low Eta Sum

Entries 0



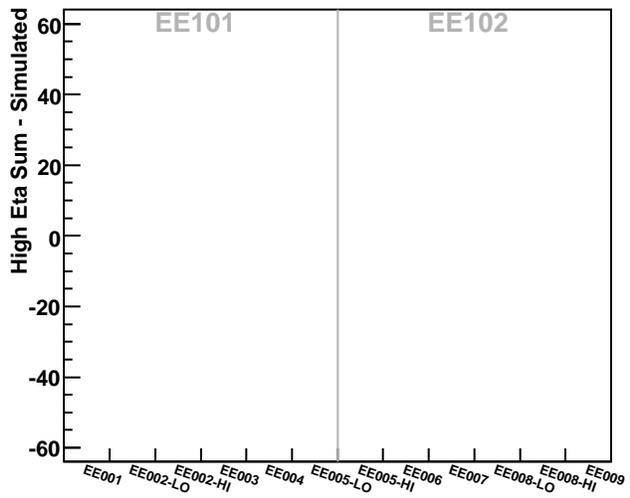
Endcap EMC L1 Input - High Eta Sum

Entries 1.68e+07



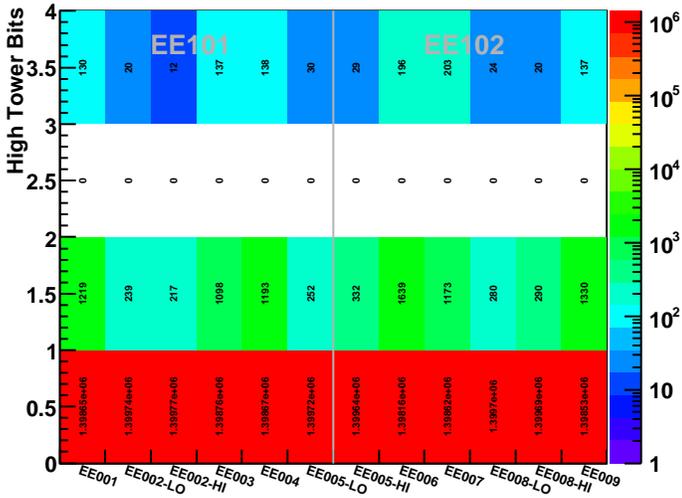
Endcap EMC L1 Input - High Eta Sum

Entries 0



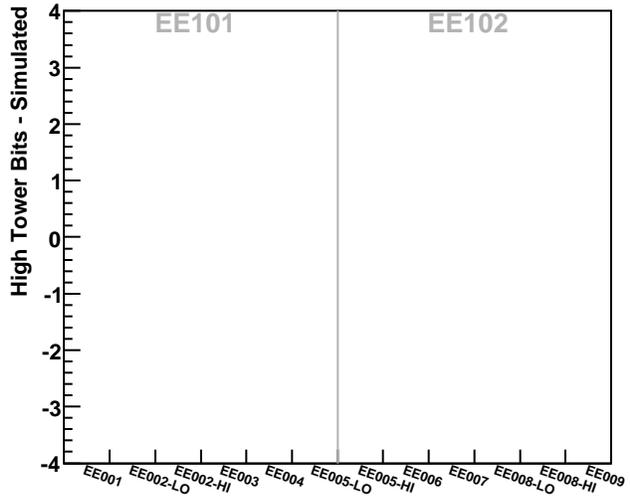
Endcap EMC L1 Input - High Tower Bits

Entries 1.68e+07



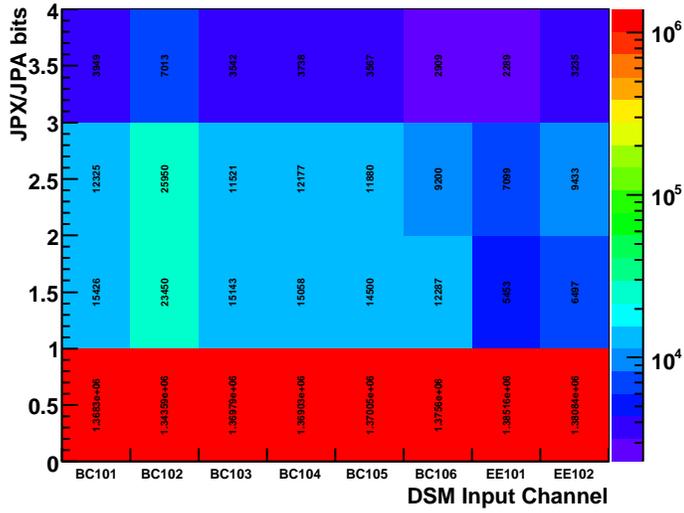
Endcap EMC L1 Input - High Tower Bits

Entries 0



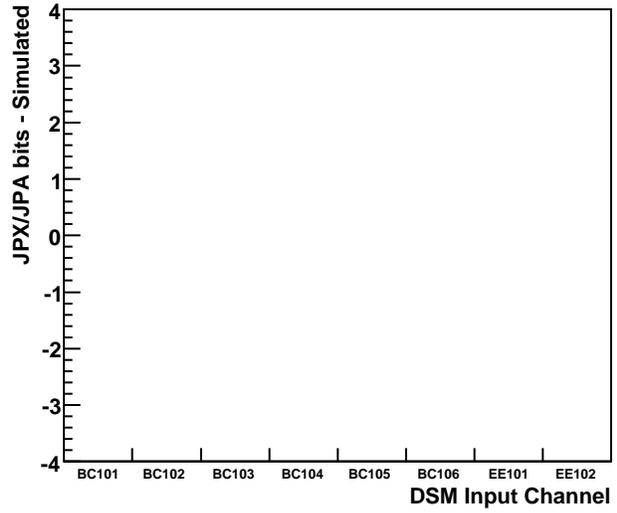
EMC L2 Input - JPX/JPA bits

Entries 1.12e+07



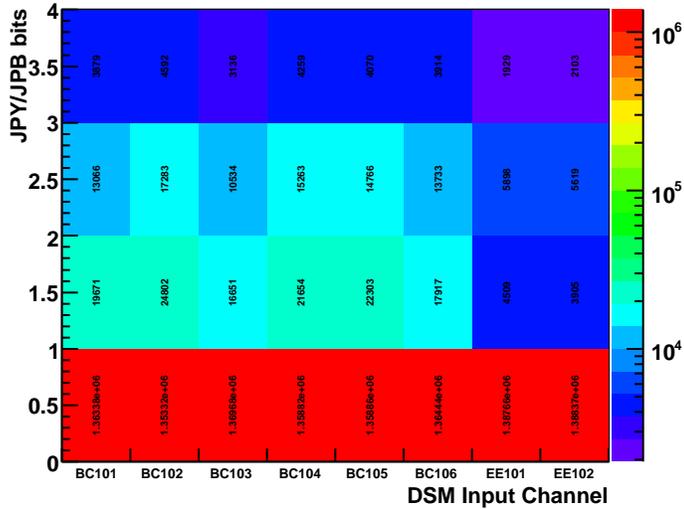
EMC L2 Input - JPX/JPA bits

Entries 0



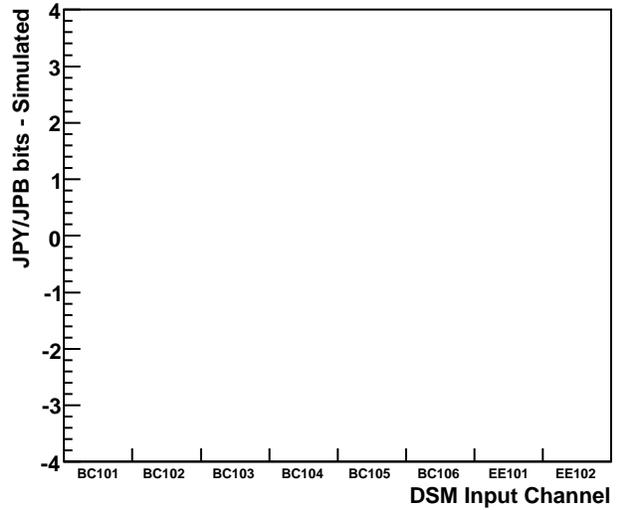
EMC L2 Input - JPY/JPB bits

Entries 1.12e+07



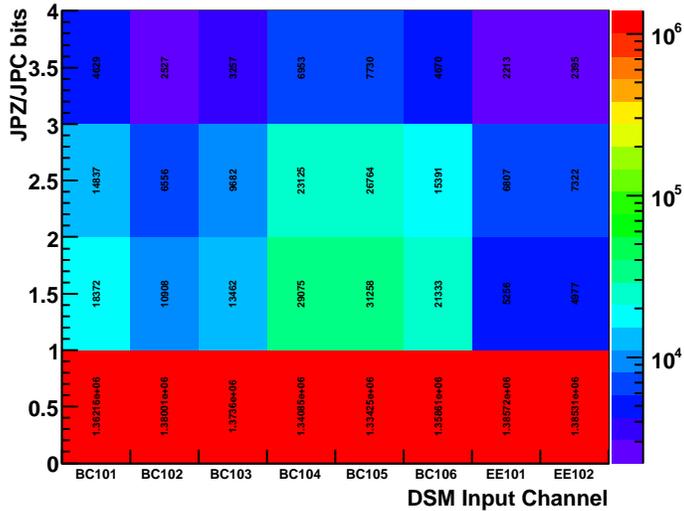
EMC L2 Input - JPY/JPB bits

Entries 0



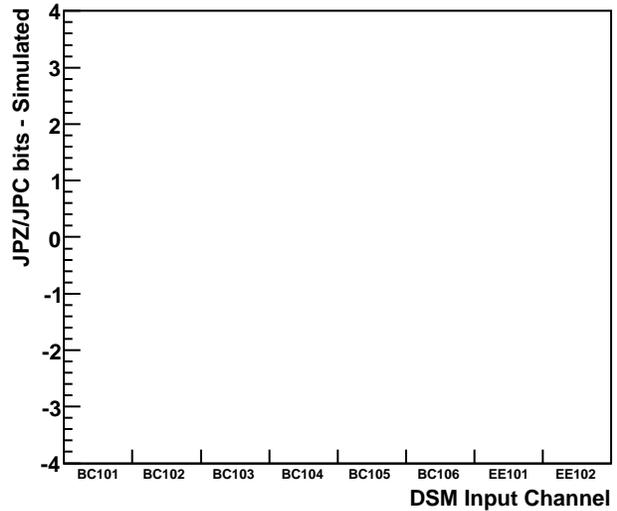
EMC L2 Input - JPZ/JPC bits

Entries 1.12e+07



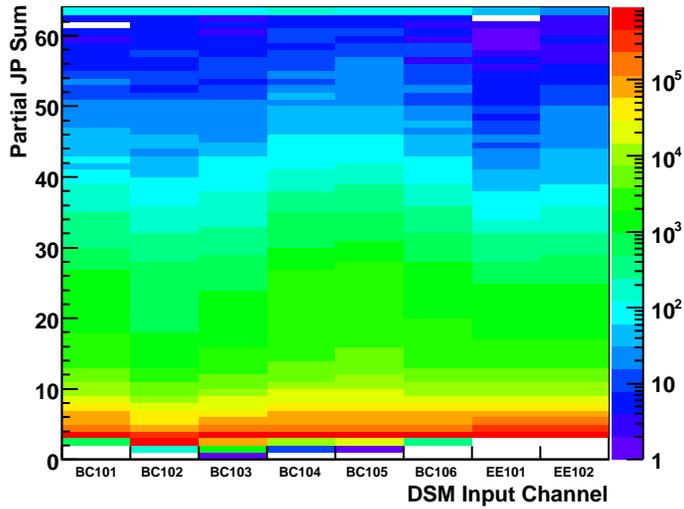
EMC L2 Input - JPZ/JPC bits

Entries 0



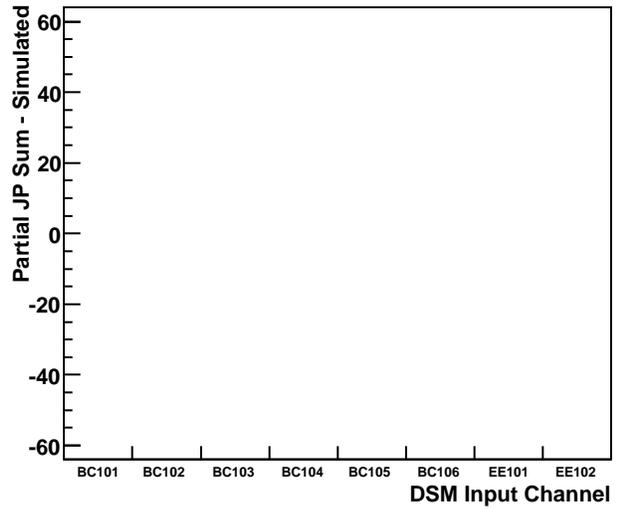
EMC L2 Input - Partial JP Sum

Entries 1.12e+07



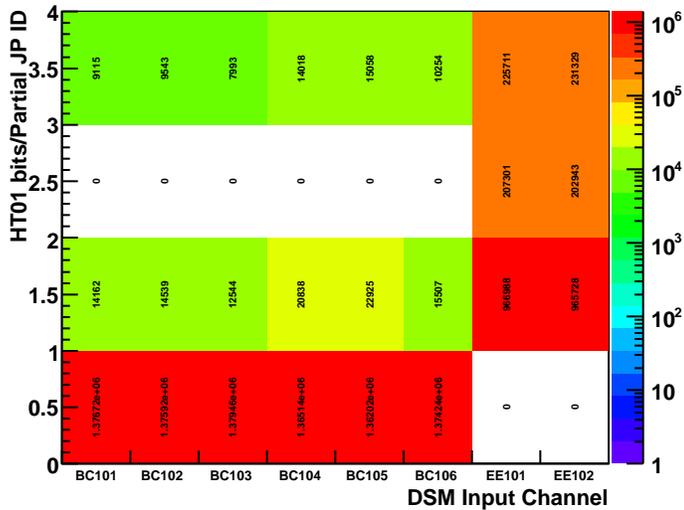
EMC L2 Input - Partial JP Sum

Entries 0



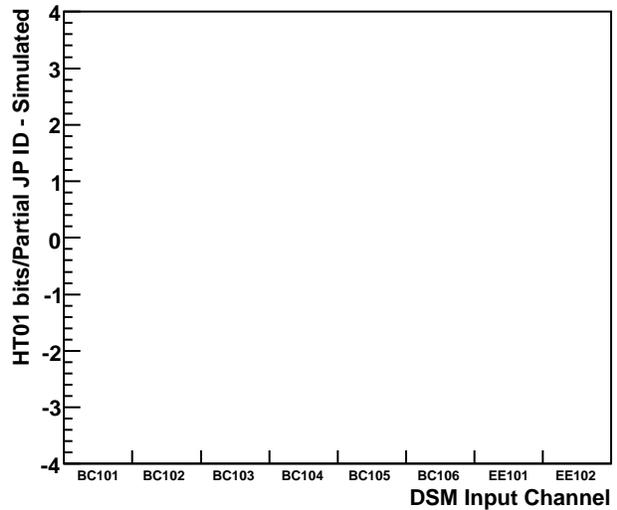
EMC L2 Input - HT01 bits/Partial JP ID

Entries 1.12e+07



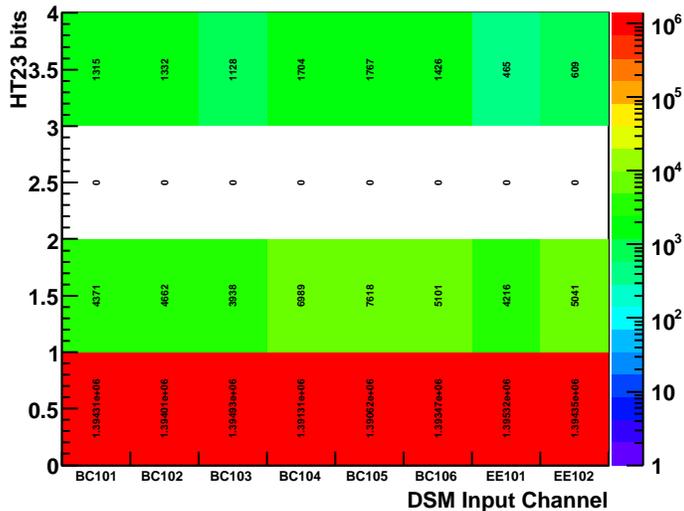
EMC L2 Input - HT01 bits/Partial JP ID

Entries 0



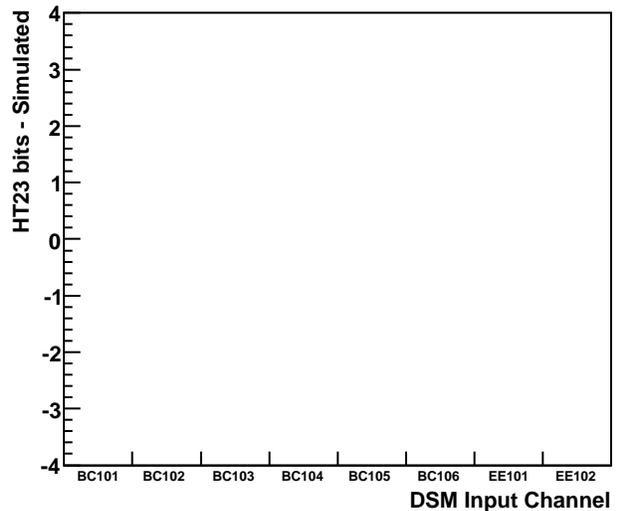
EMC L2 Input - HT23 bits

Entries 1.12e+07



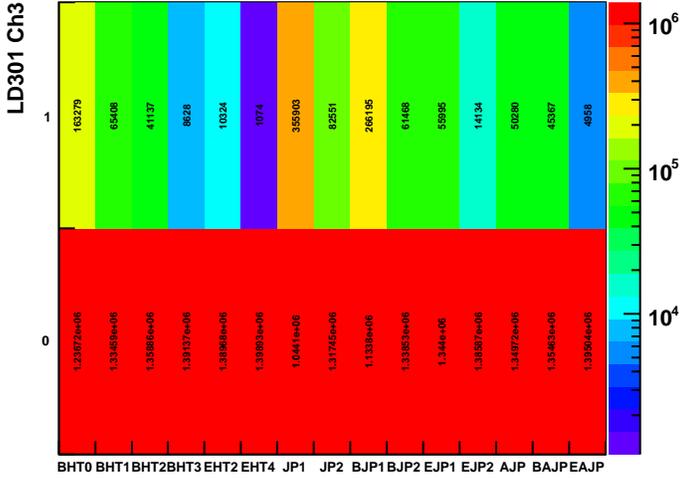
EMC L2 Input - HT23 bits

Entries 0



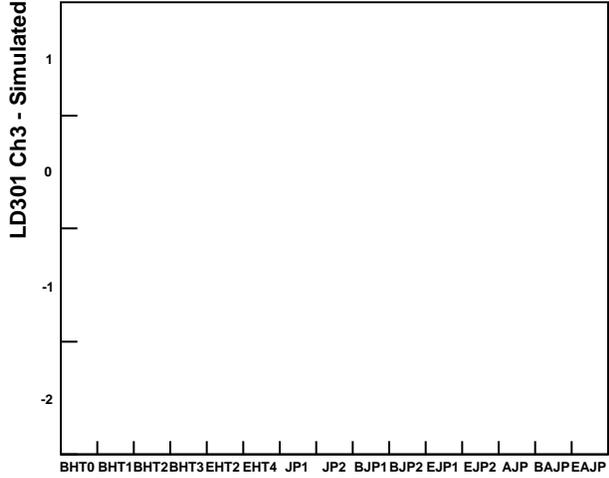
Last DSM Ch3 Input

Entries 2.1e+07



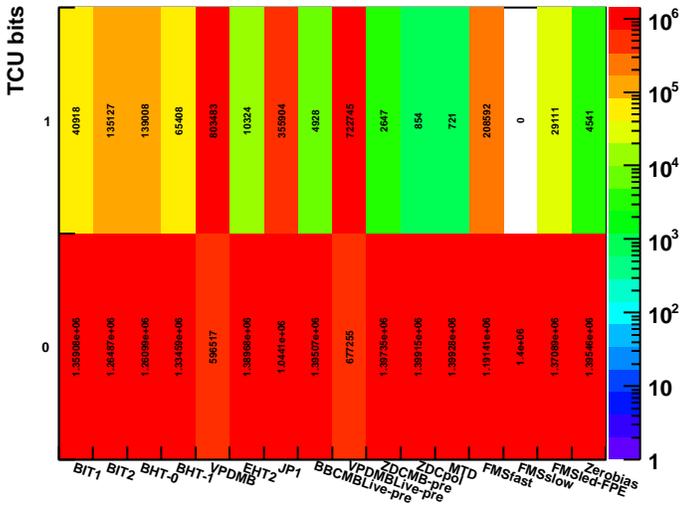
Last DSM Ch3 Input

Entries 0



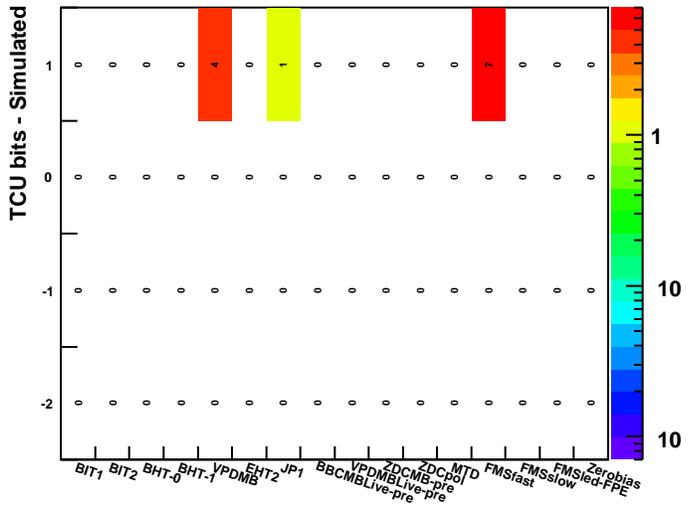
TCU bits

Entries 2.24e+07



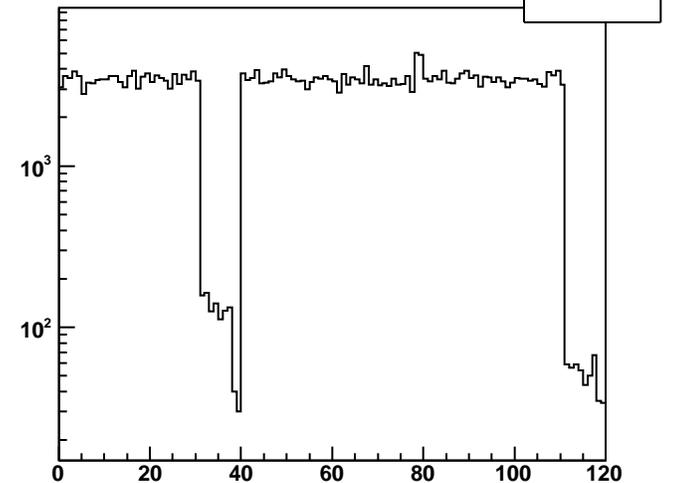
TCU bits

Entries 12



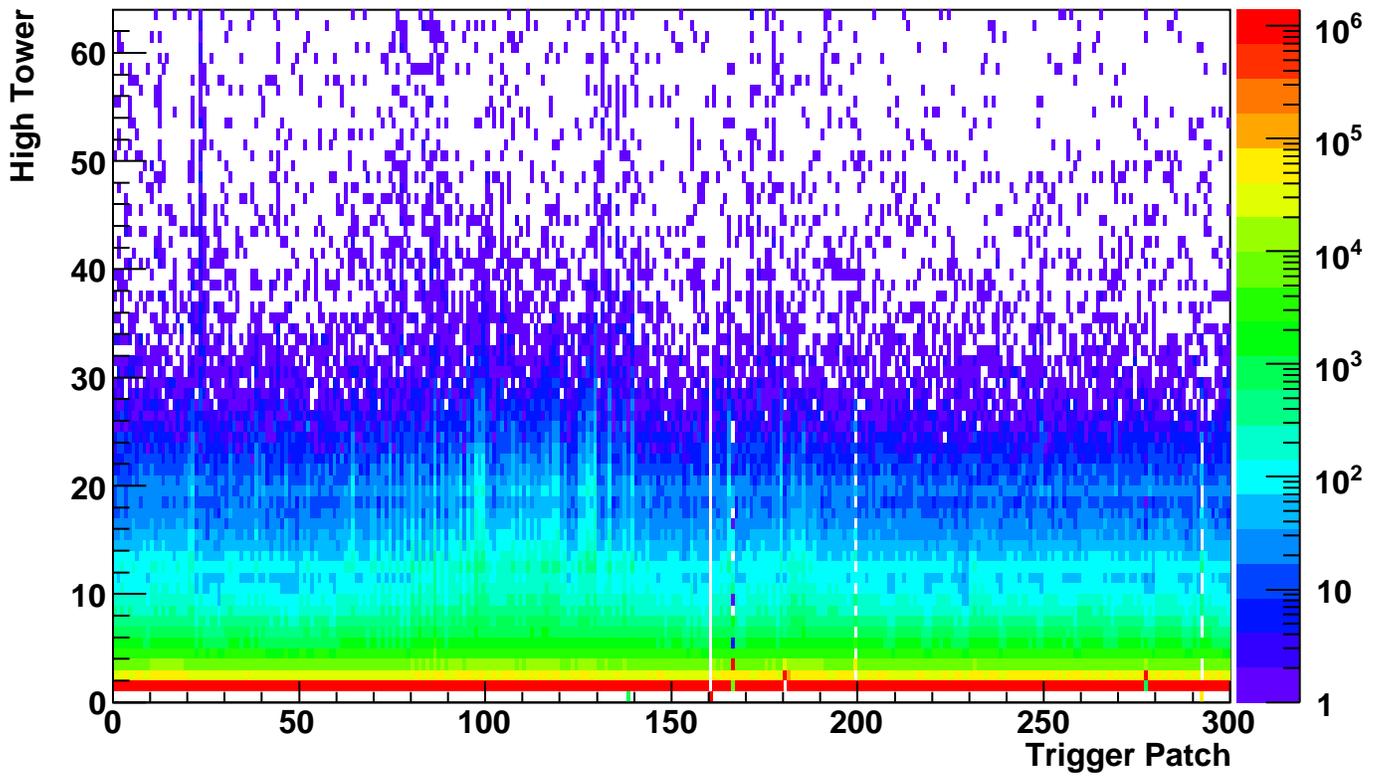
BunchId7Bit (JP1)

Entries 355903



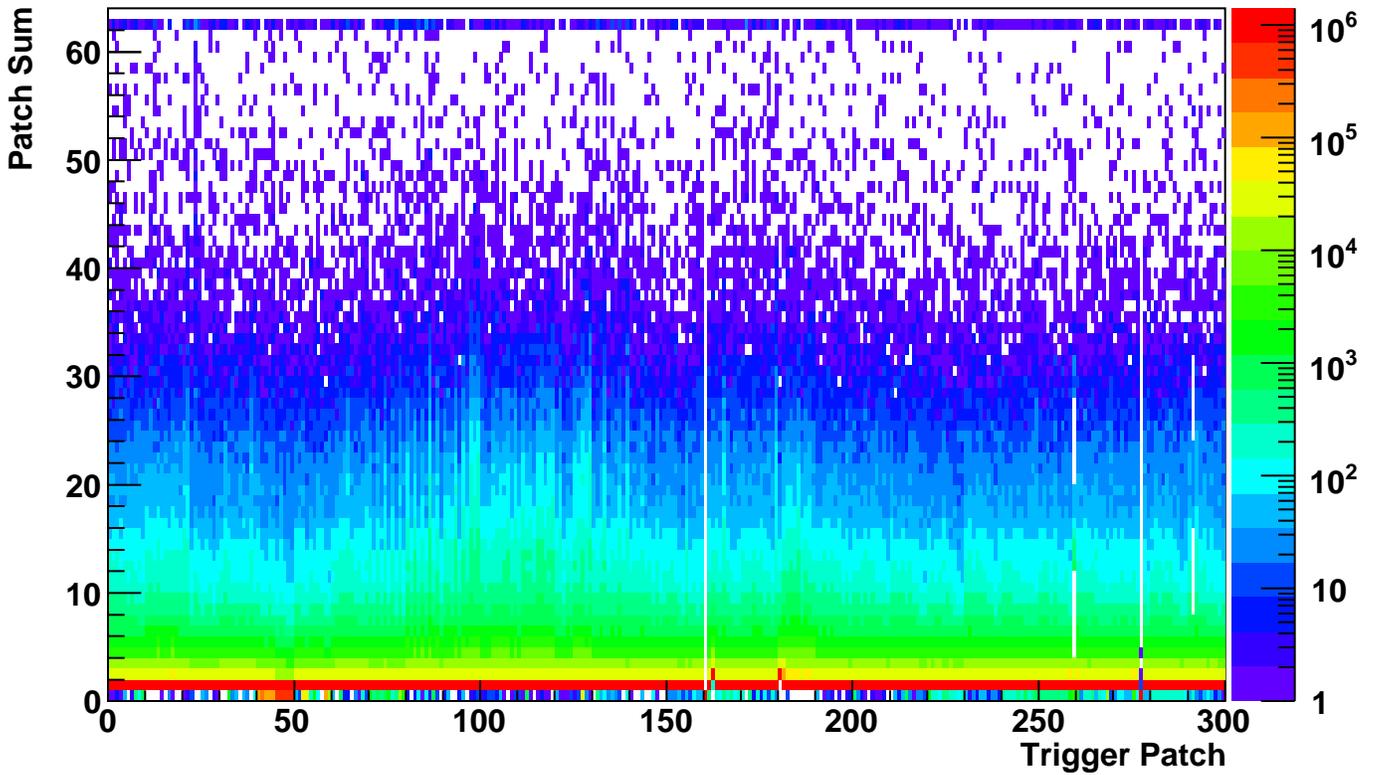
Barrel EMC L0 Input - High Tower

Entries 4.2e+08



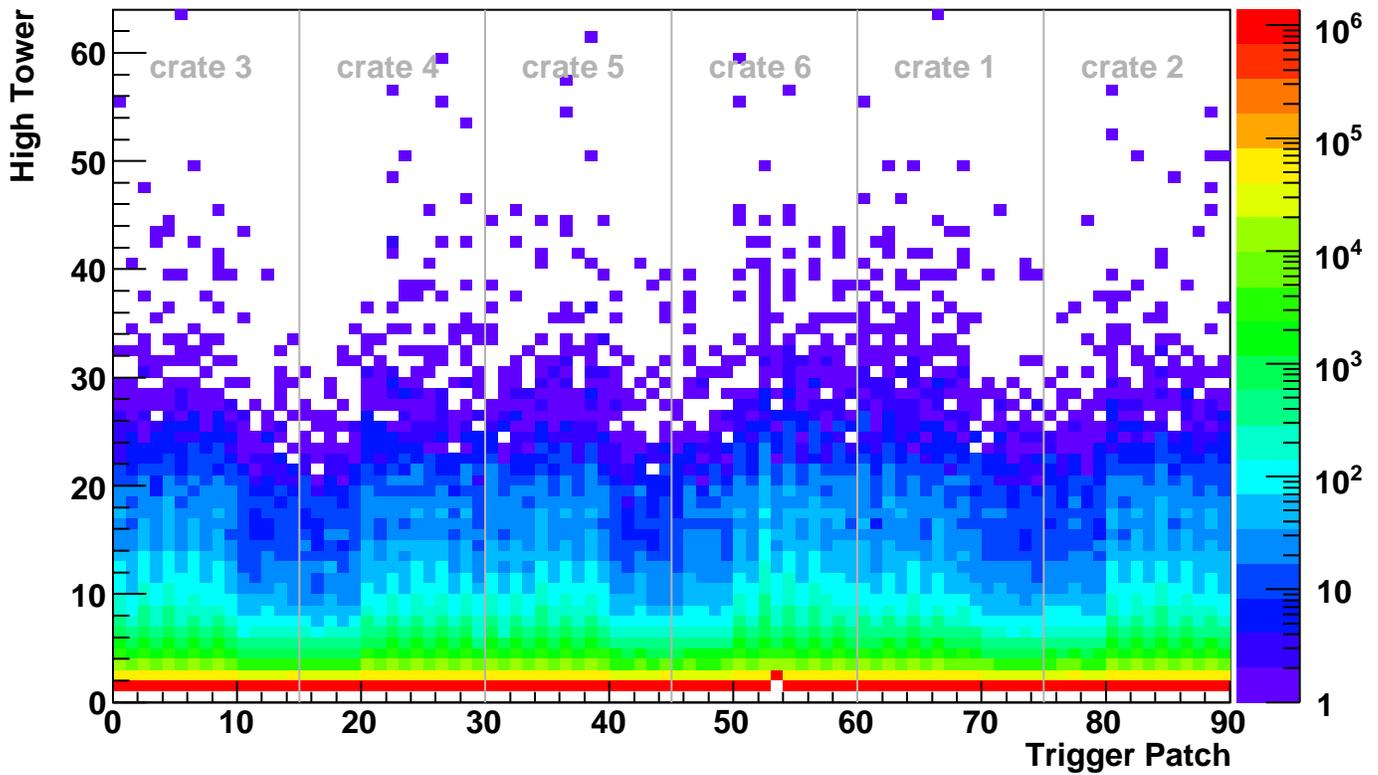
Barrel EMC L0 Input - Patch Sum

Entries 4.2e+08



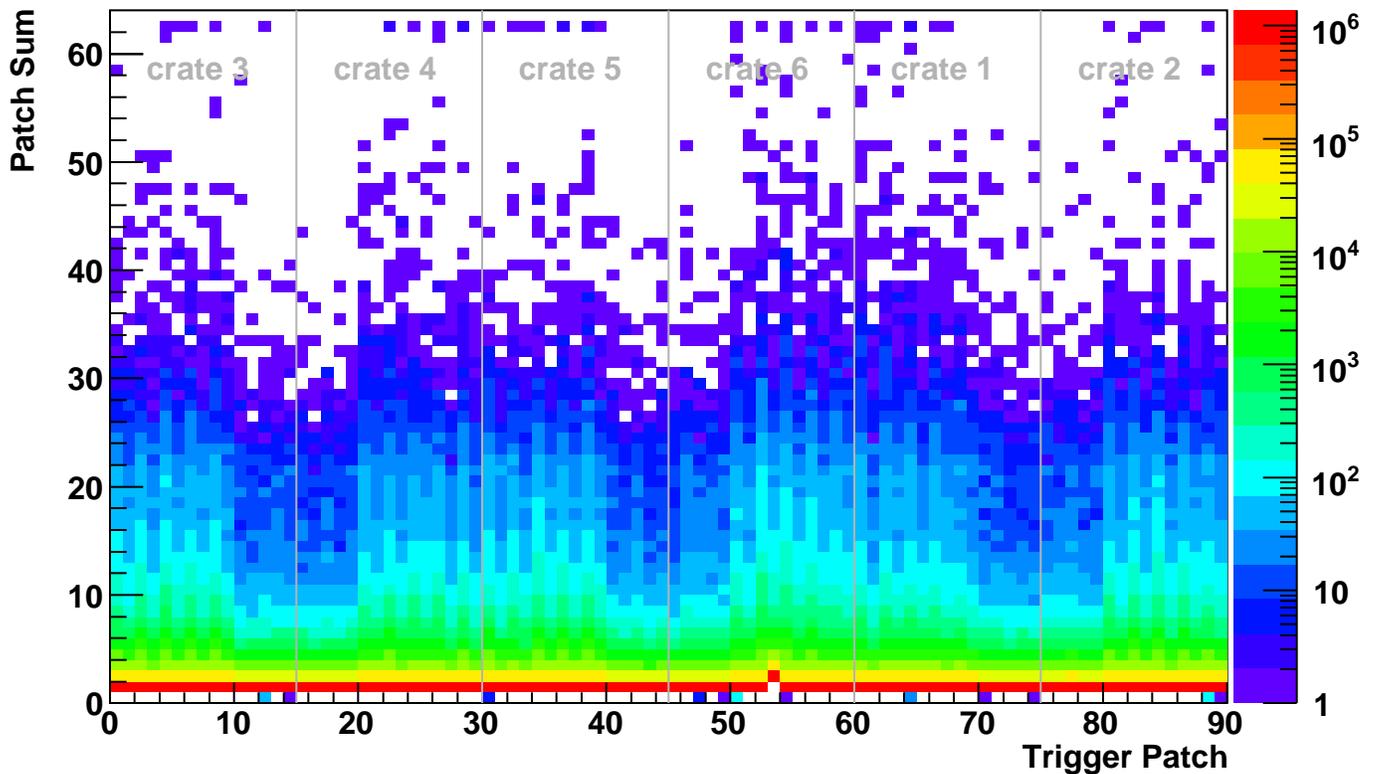
Endcap EMC L0 Input - High Tower

Entries 1.26e+08



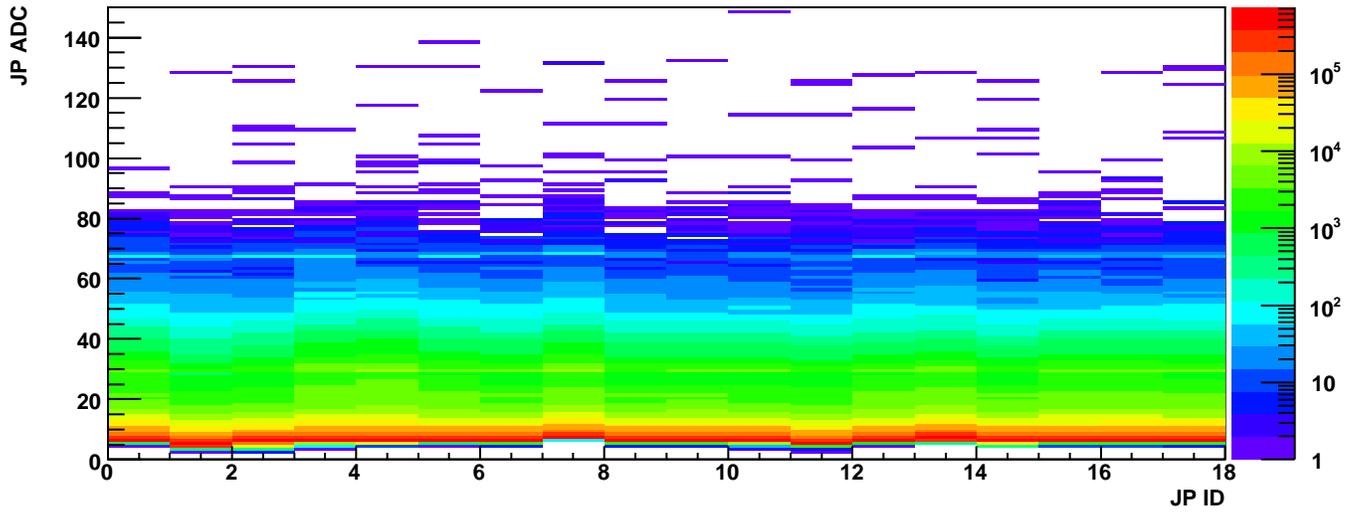
Endcap EMC L0 Input - Patch Sum

Entries 1.26e+08



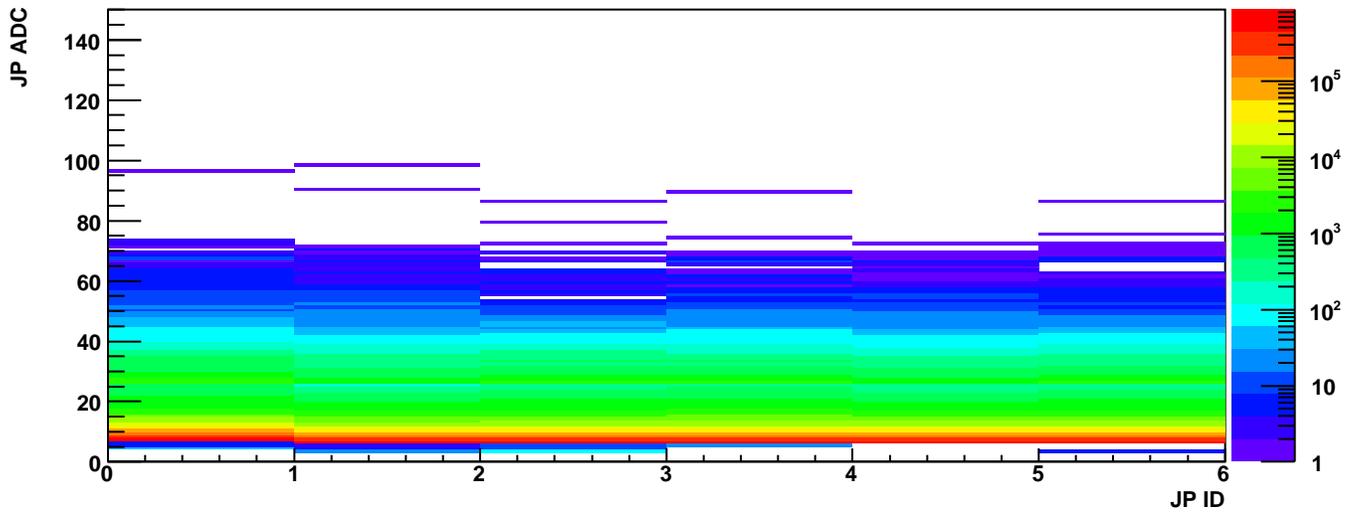
Barrel Jet Patches

Entries 2.52e+07



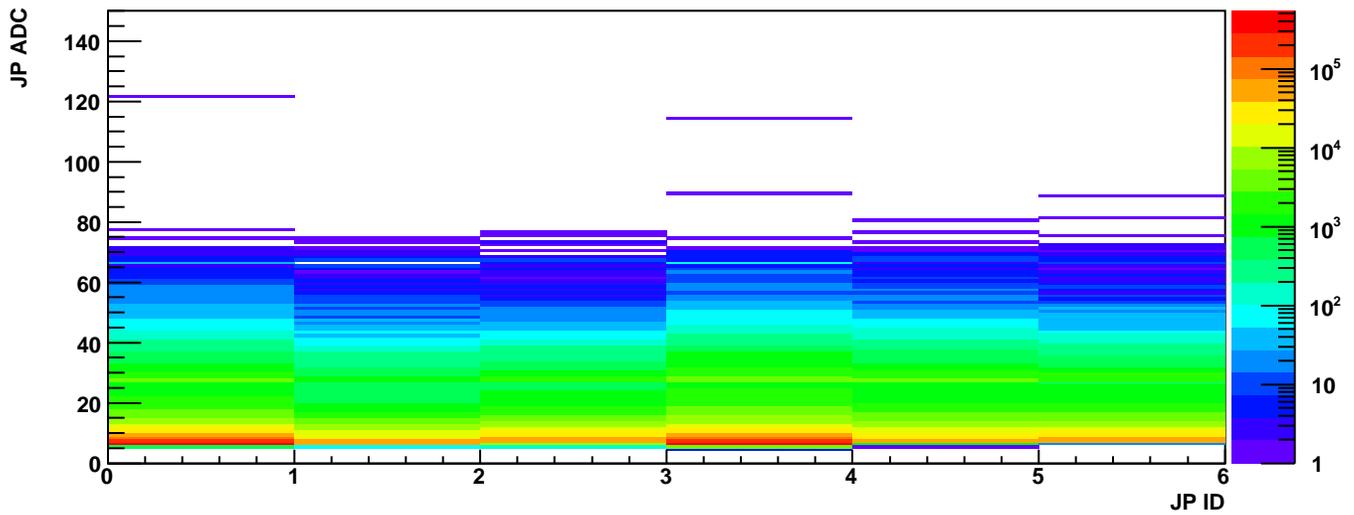
Endcap Jet Patches

Entries 8400000



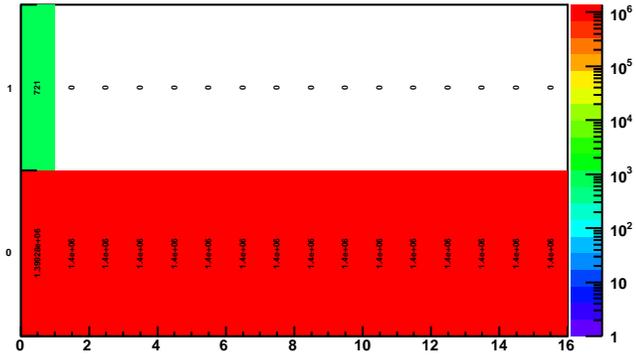
Hybrid Jet Patches

Entries 2800000



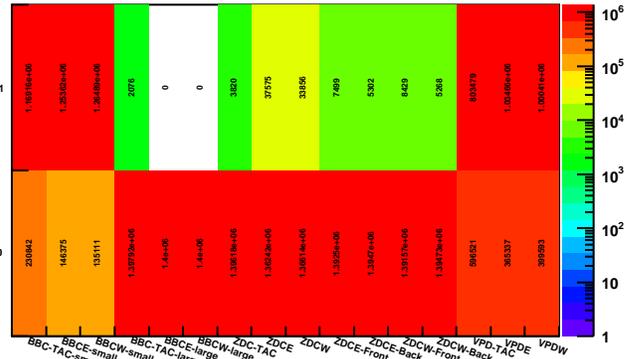
TF201 0-15 (ch0)

Entries 2.24e+07



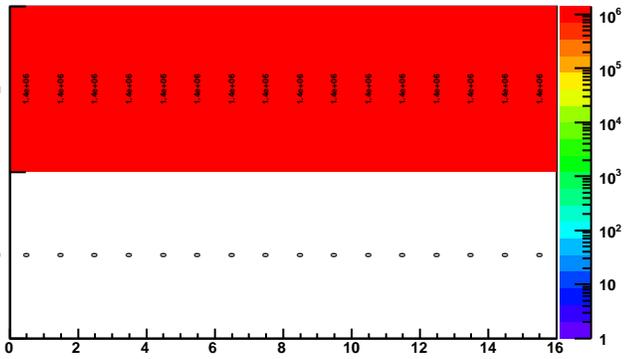
VT201 0-15 (ch1)

Entries 2.24e+07



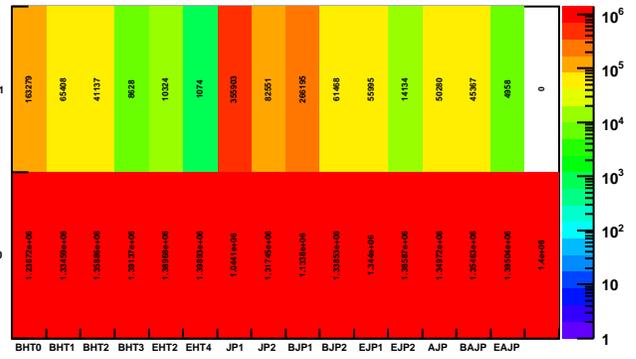
TF201 16-31 (ch2)

Entries 2.24e+07



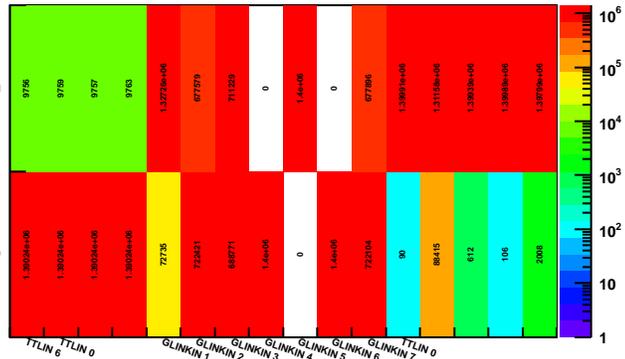
EM201 0-15 (ch3)

Entries 2.24e+07



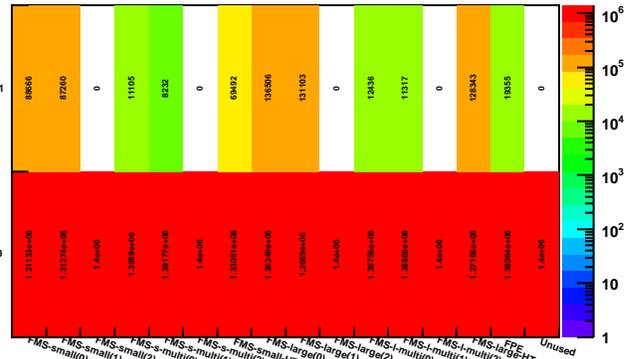
RAT board (ch4)

Entries 2.24e+07



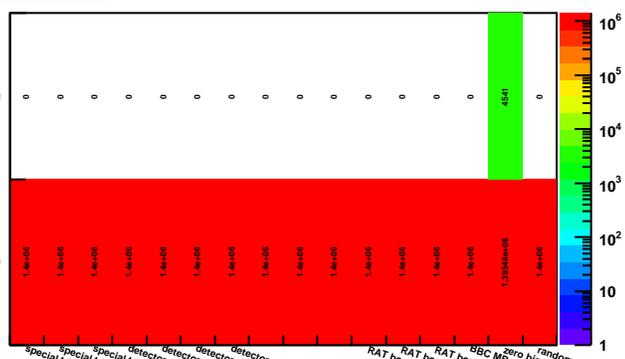
FP201 0-15 (ch5)

Entries 2.24e+07



ST201 0-15 (ch6)

Entries 2.24e+07



Unused (ch7)

Entries 2.24e+07

